



US 20060139257A1

(19) **United States**

(12) **Patent Application Publication** (10) **Pub. No.: US 2006/0139257 A1**

Kwak et al.

(43) **Pub. Date:** **Jun. 29, 2006**

(54) **PIXEL CIRCUIT AND ORGANIC LIGHT
EMITTING DISPLAY**

Publication Classification

(76) Inventors: **Won Kyu Kwak**, Seongnam (KR);
Sung Cheon Park, Suwon (KR)

(51) **Int. Cl.**
G09G 3/30 (2006.01)
(52) **U.S. Cl.** **345/76**

Correspondence Address:
H.C. PARK & ASSOCIATES, PLC
8500 LEESBURG PIKE
SUITE 7500
VIENNA, VA 22182 (US)

(21) Appl. No.: **11/296,339**

(57) **ABSTRACT**

(22) Filed: **Dec. 8, 2005**

(30) **Foreign Application Priority Data**

Dec. 9, 2004 (KR) 10-2004-0103817

An organic light emitting display includes a pixel circuit having first, second, and third organic light emitting diodes (OLEDs), for emitting red, green, and blue light, respectively, a driving circuit commonly connected to the OLEDs, and a switching circuits connected to the OLED and the driving circuit to sequentially control the driving thereof. By controlling a plurality of OLEDs, the number of pixel circuits in an organic light emitting display is reduced, thereby reducing the number of scan lines, data lines, and emission control lines, which in turn improves the aperture ratio of the light emitting display. Further, the emission order of the OLEDs is controlled so that it is possible to prevent the generation of color breakup.

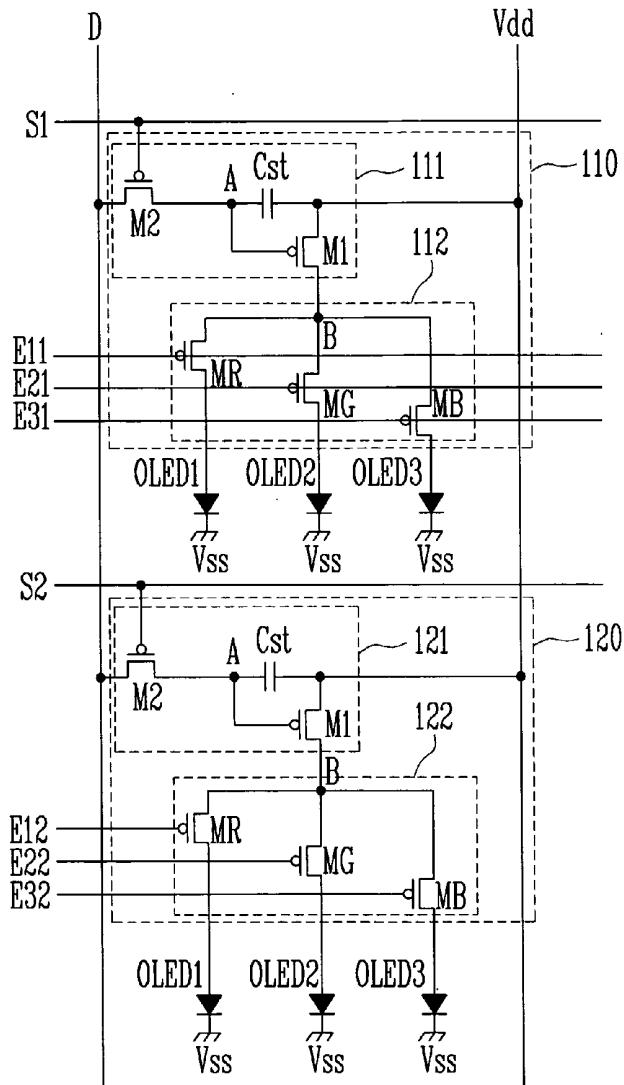


FIG. 1
(PRIOR ART)

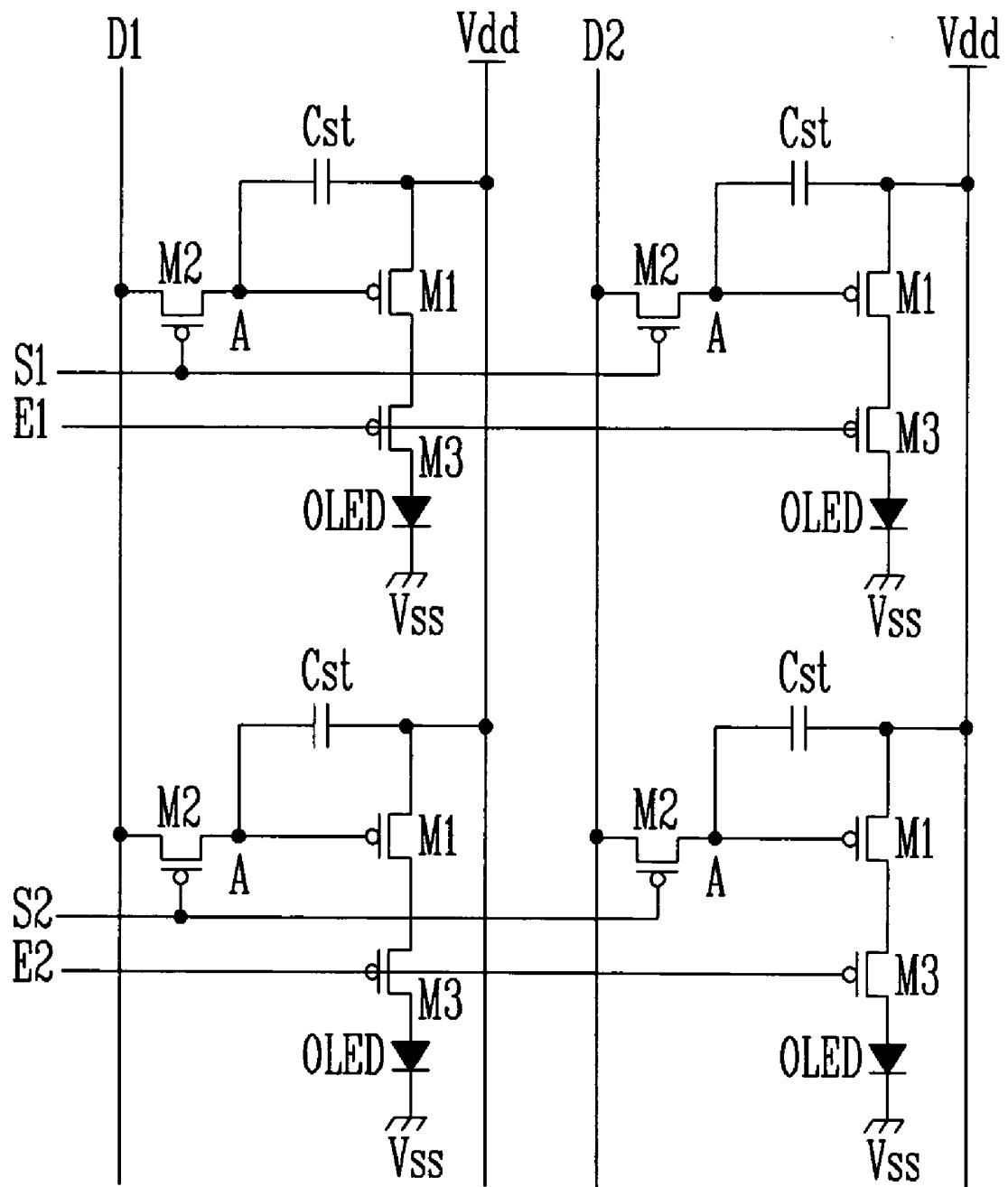


FIG. 2

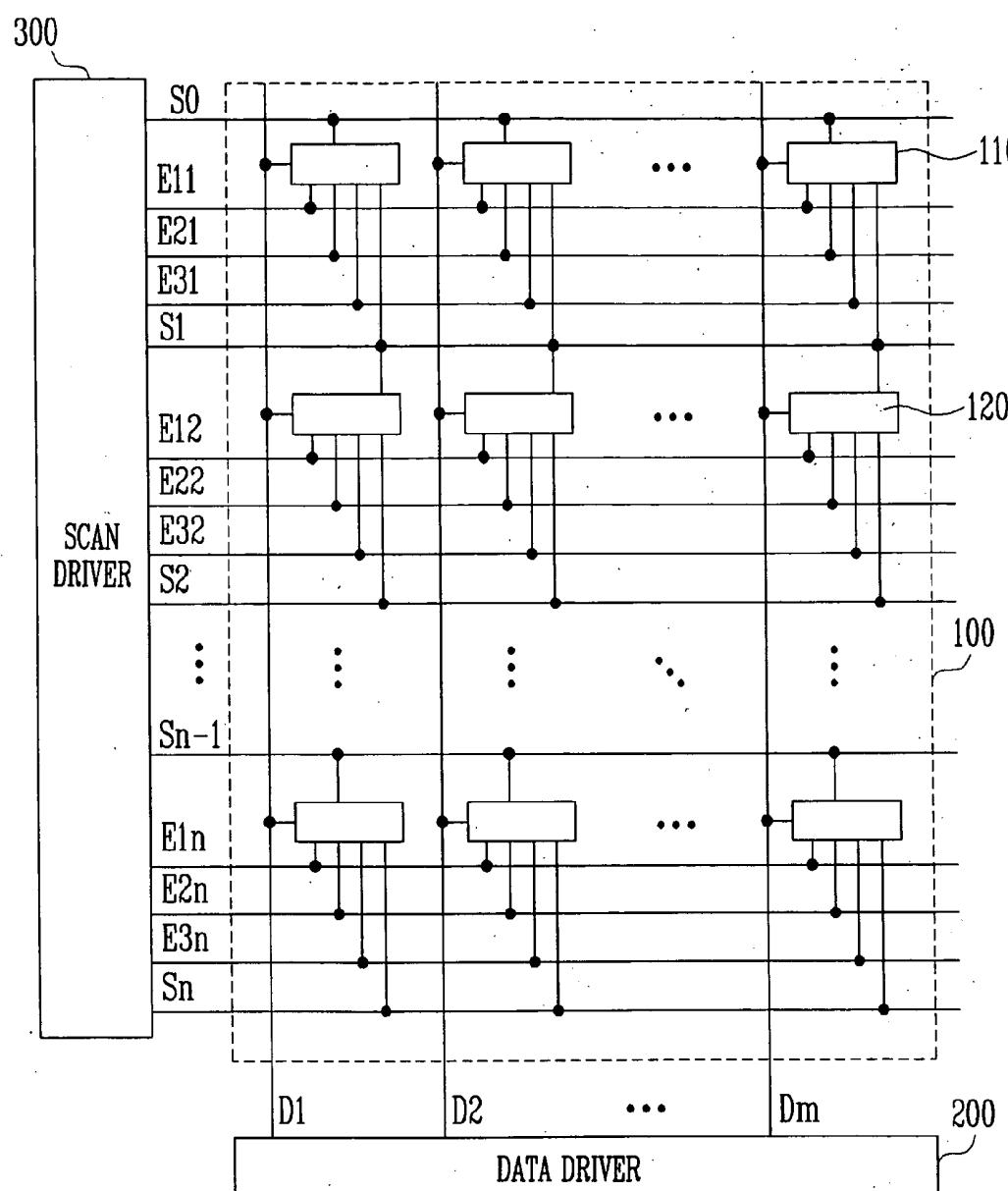


FIG. 3

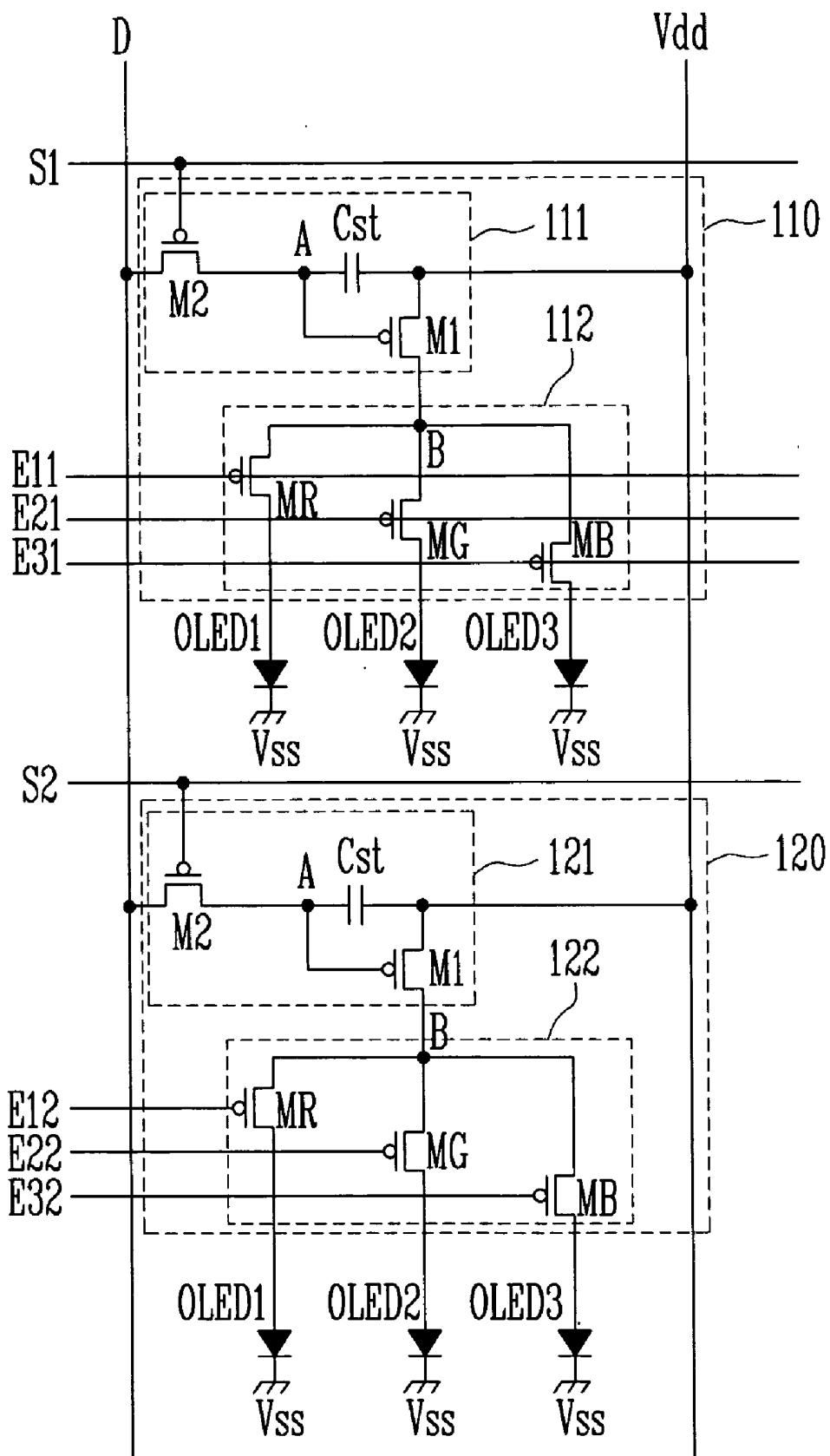


FIG. 4

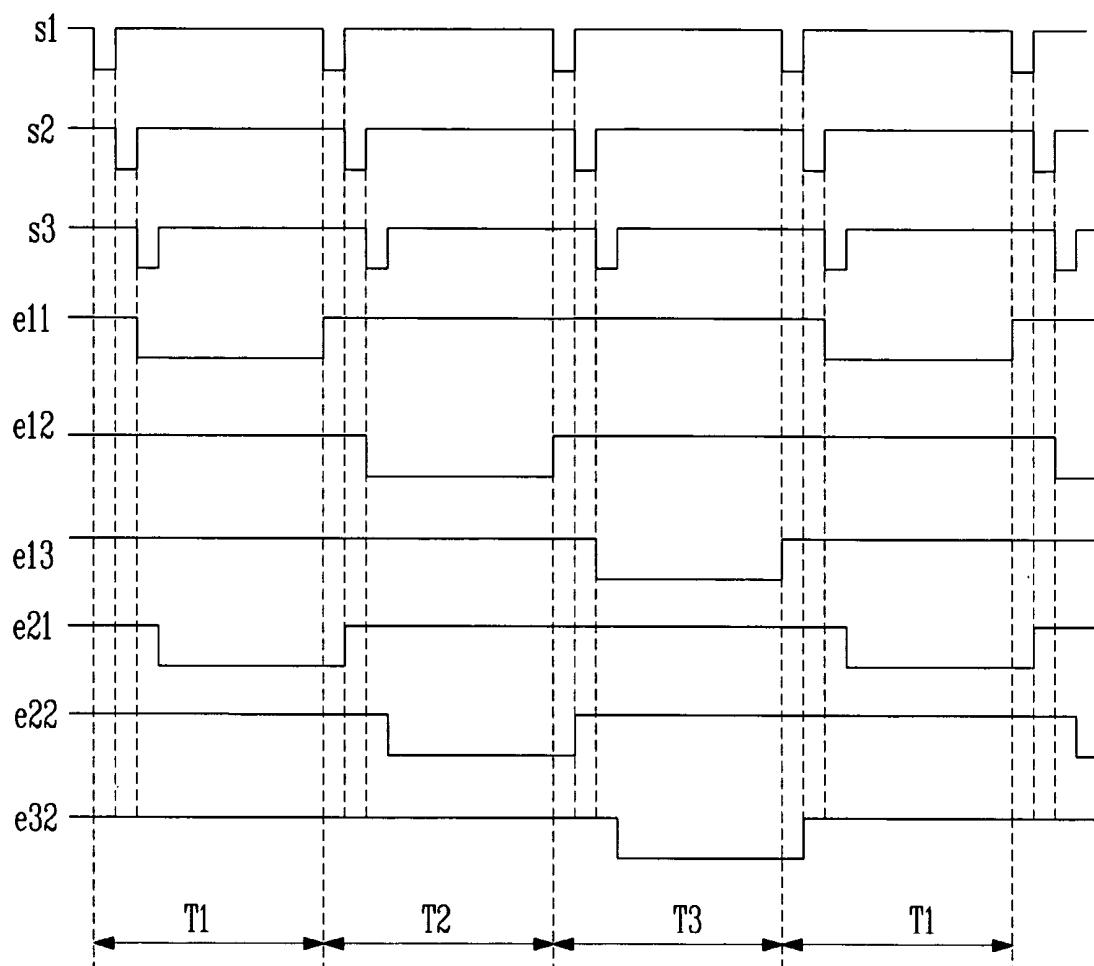


FIG. 5A

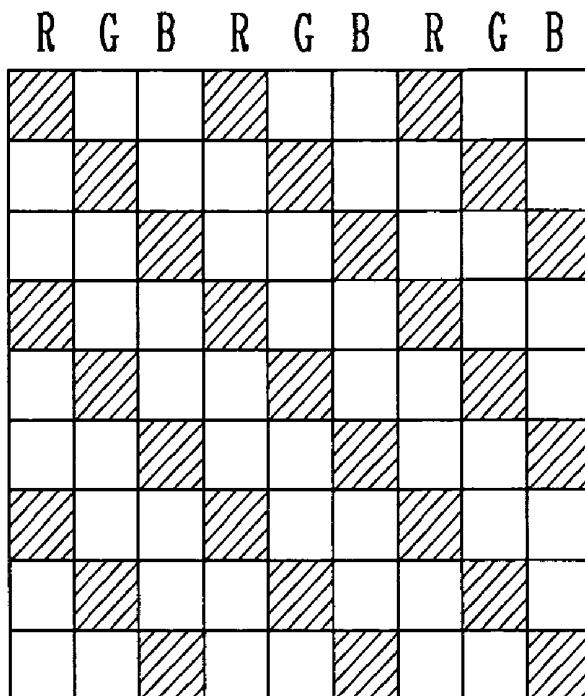


FIG. 5B

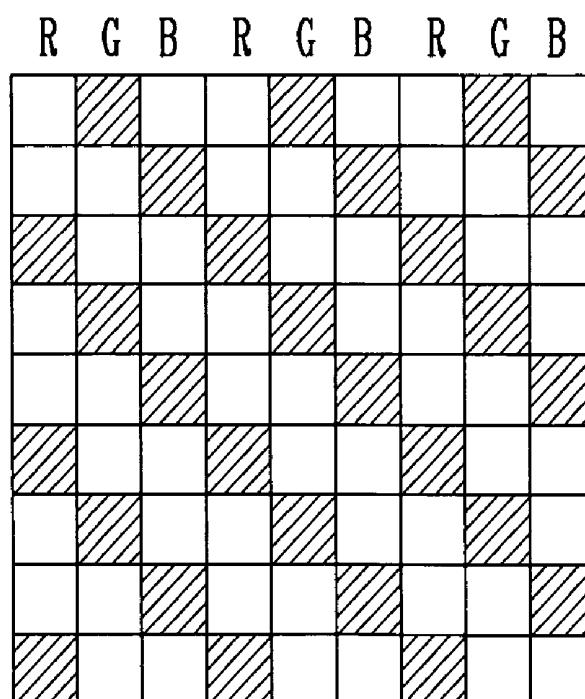


FIG. 5C

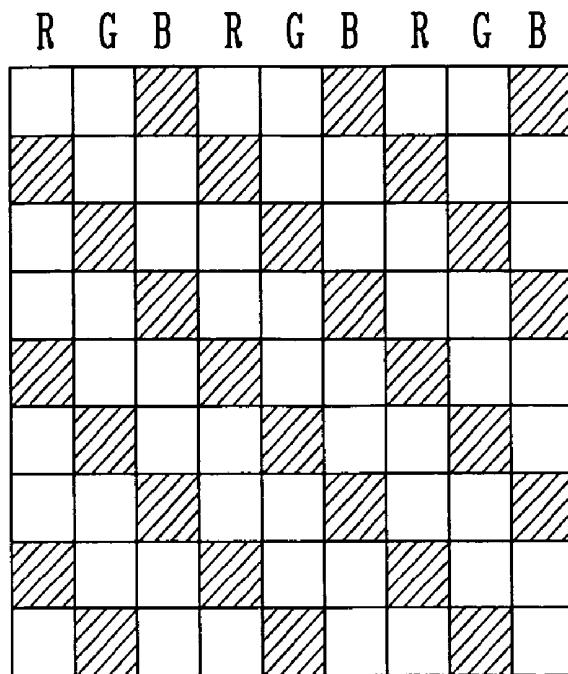


FIG. 6A

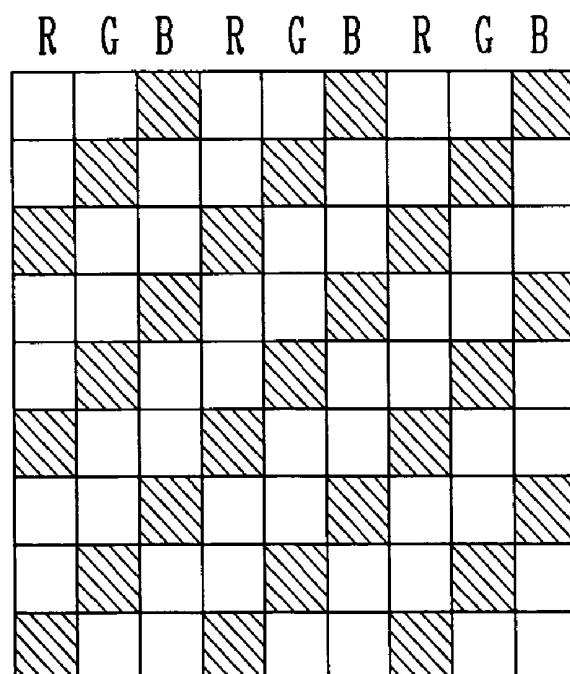


FIG. 6B

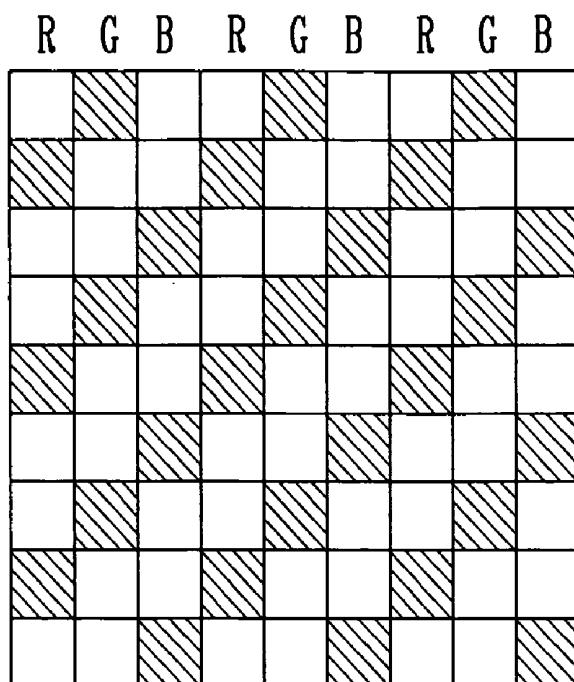


FIG. 6C

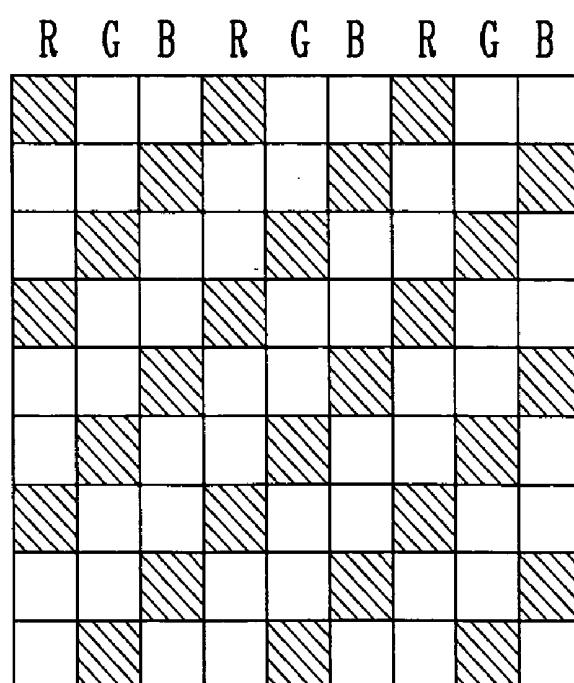


FIG. 7

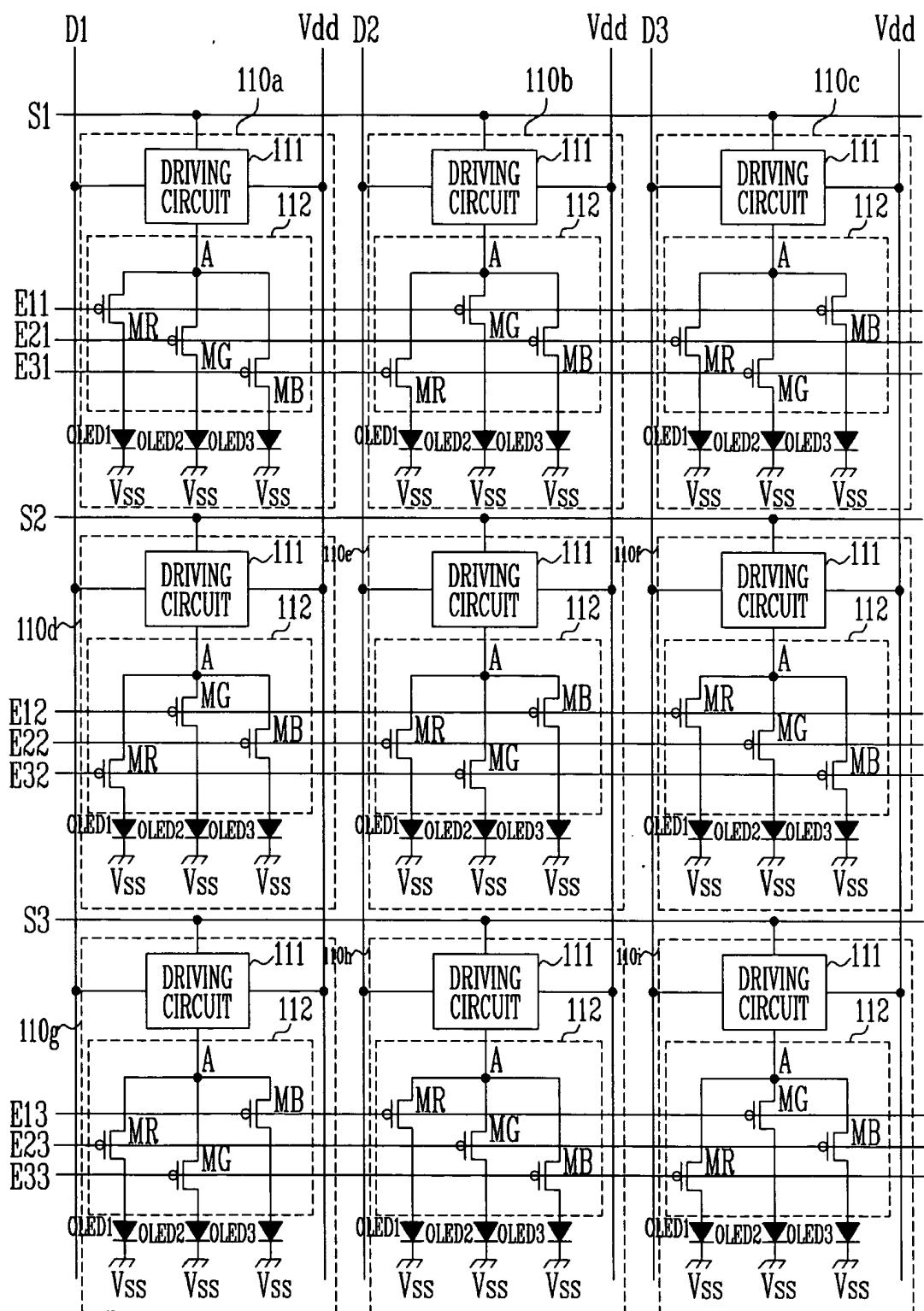


FIG. 8

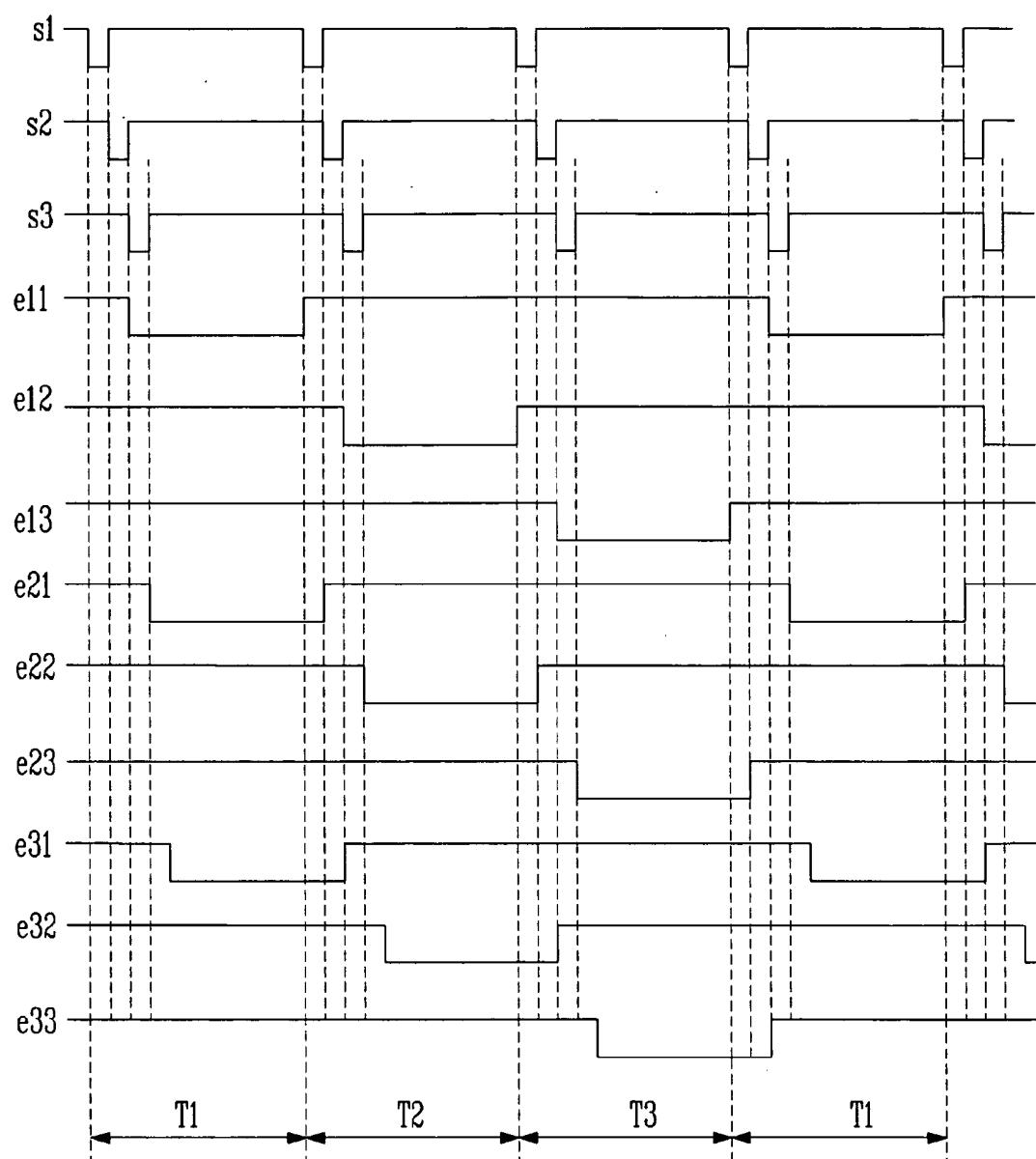


FIG. 9A

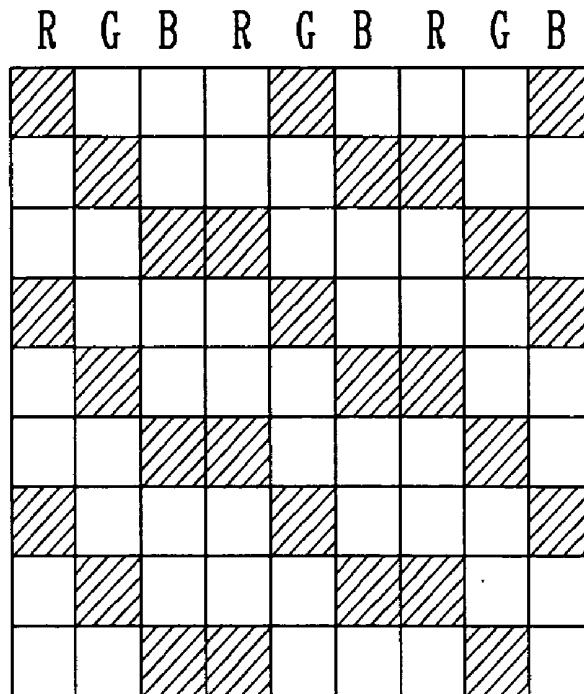


FIG. 9B

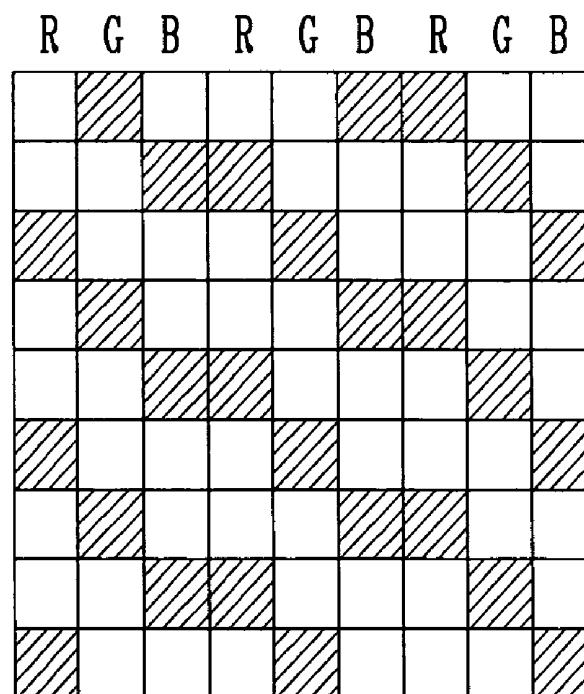


FIG. 9C

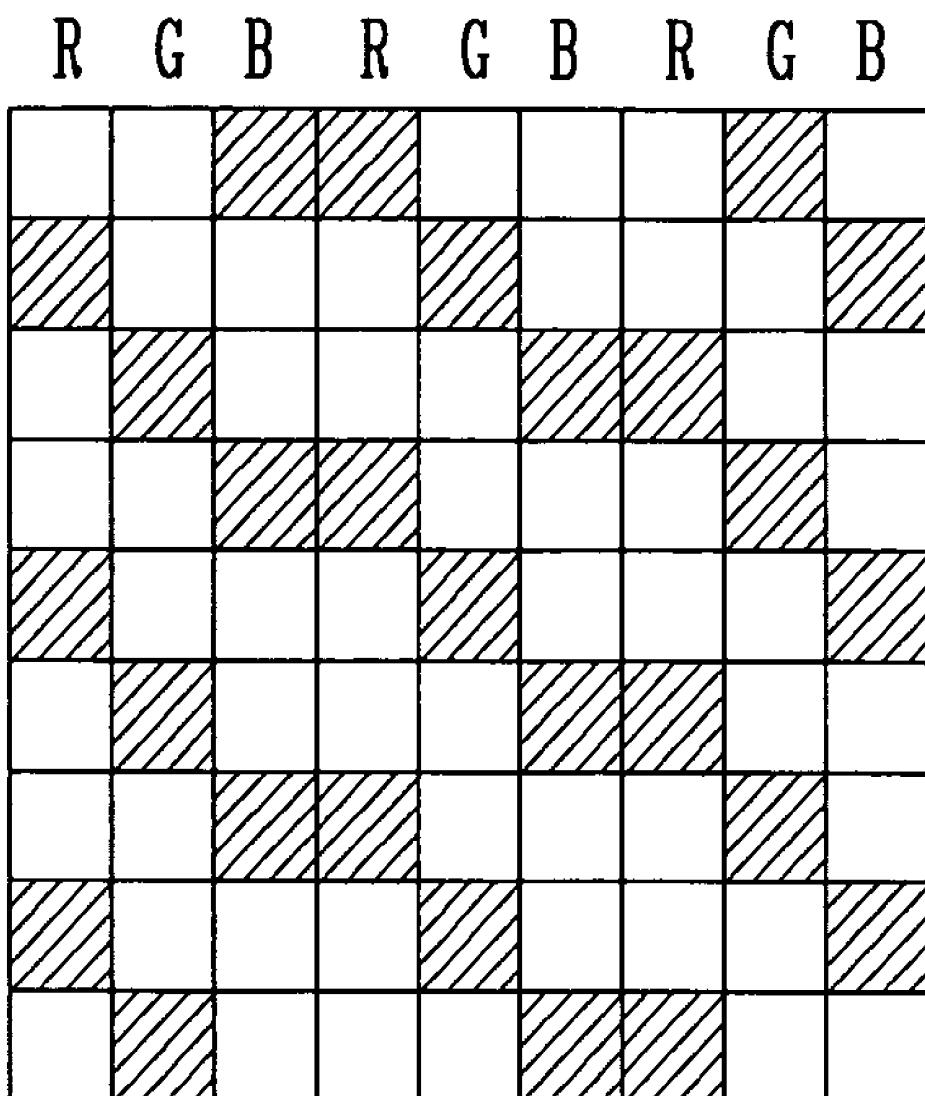


FIG. 10

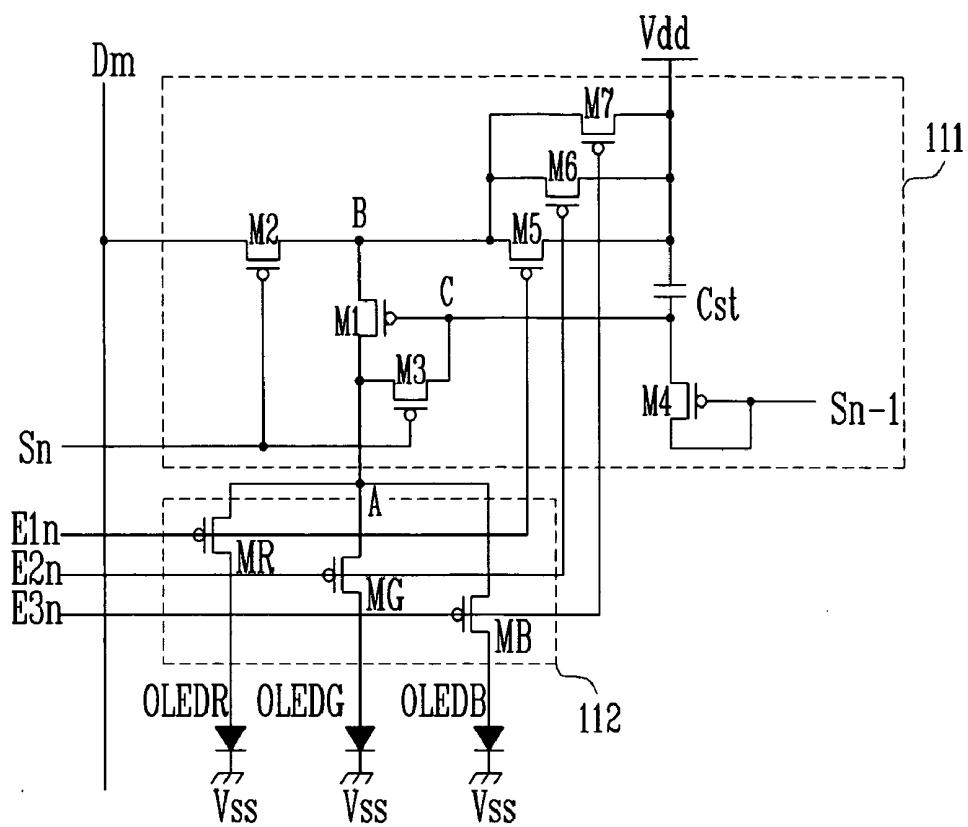


FIG. 11

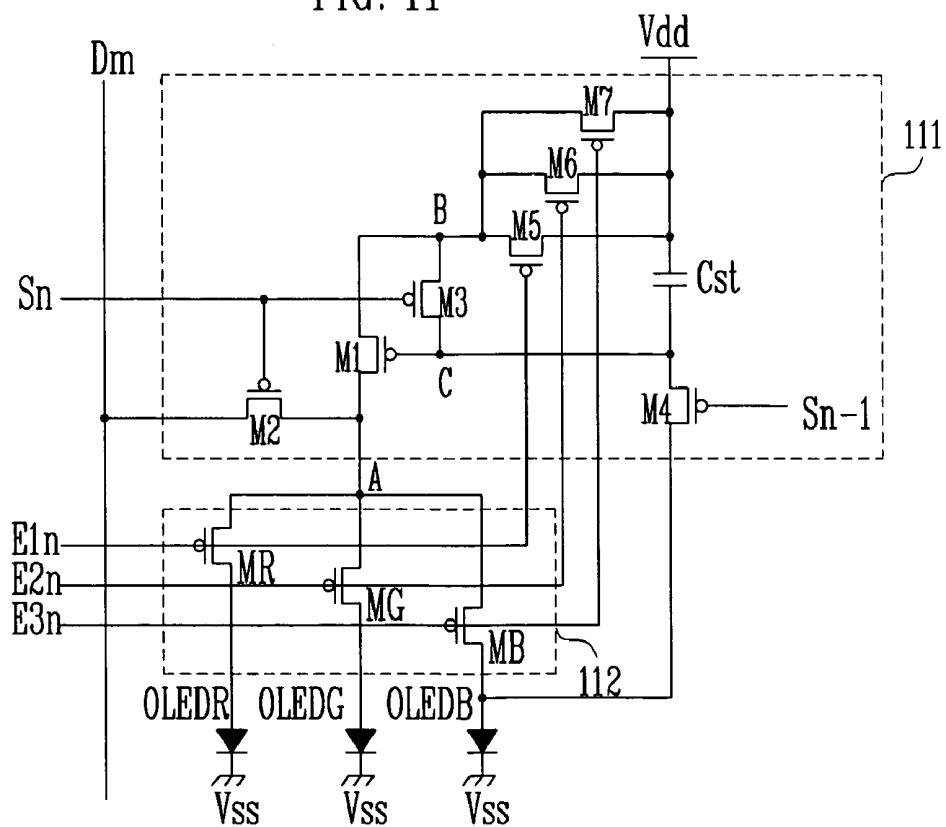


FIG. 12

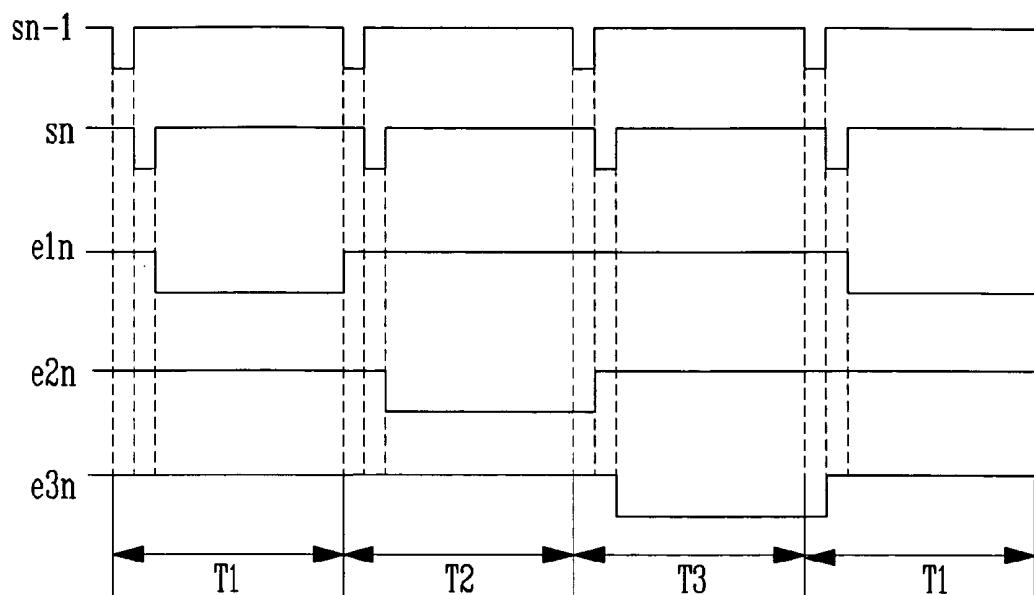
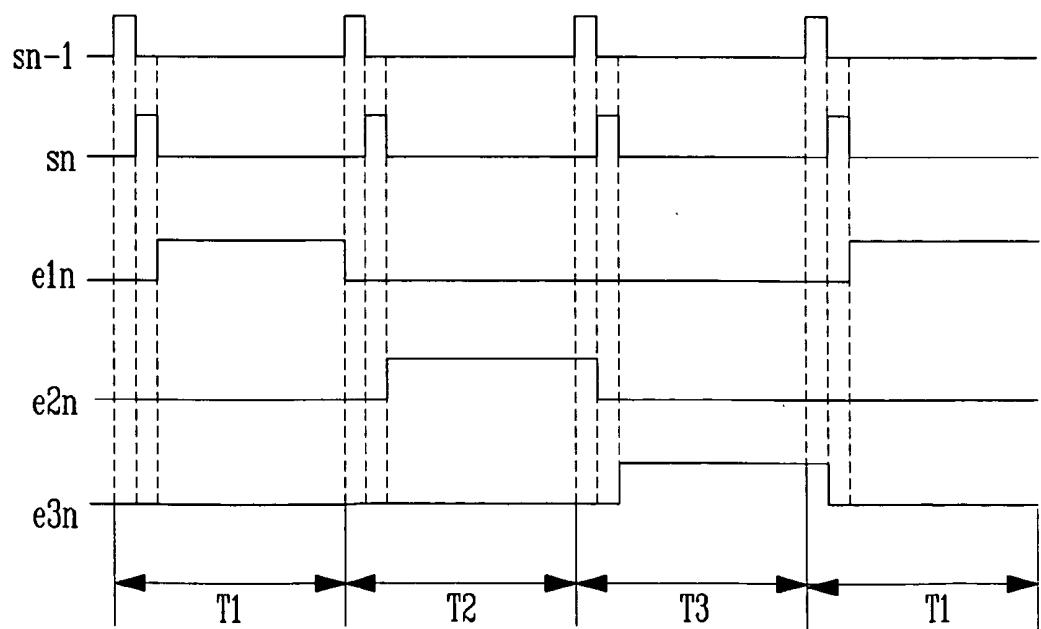


FIG. 13



PIXEL CIRCUIT AND ORGANIC LIGHT EMITTING DISPLAY

CROSS-REFERENCE TO RELATED APPLICATIONS

[0001] This application claims the benefit of and priority to Korean Patent Application No. 10-2004-103817, filed on Dec. 9, 2004, in the Korean Intellectual Property Office, the disclosure of which is incorporated herein by reference.

BACKGROUND

[0002] 1. Field of the Invention

[0003] The present invention relates to a pixel circuit and an organic light emitting display, and in particular, a pixel circuit connected to a plurality of organic light emitting diodes (OLEDs) that emit light so that it is possible to improve the aperture ratio of the light emitting display using such a pixel circuit.

[0004] 2. Discussion of Related Art

[0005] Recently, flat panel displays have been developed, that are of reduced weight and volume as compared with displays using cathode ray tubes (CRT). Highlighted are organic light emitting displays having improved luminous efficiency, brightness, and view angle and high response speed.

[0006] An OLED has a structure in which an emission layer that may be a light emitting thin film is positioned between a cathode electrode and an anode electrode. Electrons and corresponding holes are injected into the emission layer so that they are recombined to generate exciters whose energy is reduced. As a result, light is emitted.

[0007] In the OLED, the emission layer is formed of either organic or inorganic material. Types of OLEDs are divided into an inorganic OLEDs and an organic OLEDs according to the emission layer material.

[0008] Referring to **FIG. 1**, four adjacent pixels are shown each that include an OLED and a pixel circuit. The pixel circuit includes a first transistor M1, a second transistor M2, a third transistor M3, and a capacitor Cst. The first, second, and third transistors M1, M2, and M3 each includes a gate, a source, and a drain. The capacitor Cst includes a first electrode and a second electrode.

[0009] Since the pixels have the same structure, the pixel shown in the upper left of **FIG. 1** will be described. The source of the first transistor M1 is connected to a power source supply line Vdd, the drain is connected to the source of the third transistor M3, and the gate is connected to a first node A. The first node A is connected to the drain of the second transistor M2. The first transistor M1 supplies current corresponding to a data signal to the OLEDs.

[0010] The source of the second transistor M2 is connected to a data line D1, the drain is connected to the first node A, and the gate is connected to a first scan line S1. The second transistor M2 transmits the data signal to the first node A in accordance with the scan signal applied to the second transistor's gate.

[0011] The source of the third transistor M3 is connected to the drain of the first transistor M1, the drain is connected to the anode electrode of the OLED, and the gate is

connected to an emission control line E1 to respond to an emission control signal. Therefore, the third transistor M3 controls the flow of current that flows from the first transistor M1 to the OLED in accordance with the emission control signal to control emission of the OLED.

[0012] The first electrode of the capacitor Cst is connected to the power source supply line Vdd while the second electrode is connected to the first node A. The capacitor Cst charges in accordance with the data signal and applies the data signal to the gate of the first transistor M1 for one frame for operation of the first transistor M1 over the frame.

[0013] However, according to the pixel used for a typical organic light emitting display, since an OLED is connected to each pixel circuit, a plurality of pixel circuits are necessary in order to emit light from a plurality of OLEDs.

[0014] Also, since one emission control line is connected to each pixel row, the aperture ratio of the organic light emitting display deteriorates.

SUMMARY OF THE INVENTION

[0015] Accordingly, the present invention provides a pixel circuit, in which a plurality of OLEDs are connected to one pixel circuit. Thus it is possible to reduce the number of pixel circuits of an organic light emitting display and thereby improve its aperture ratio. Moreover, the emission times of the plurality of OLEDs are controlled so that it is possible to minimize color breakup in an organic light emitting display using such an arrangement.

[0016] According to a first aspect of the present invention, there is provided an organic light emitting display having a first pixel, a second pixel, and a third pixel. Each pixel includes: a first, second and third OLED for emitting red, green, and blue light, respectively; a driving circuit commonly connected to the OLEDs for driving; and a switching circuit connected to the OLEDs and the driving circuit to sequentially control the driving of the first, second, and third OLEDs. The first, second, and third pixels are arranged to receive a data signal through a common data line, and an emission order of red, green, and blue light components of each pixel are different from one another. Further, the driving circuit includes: a first transistor for receiving a first power source corresponding to a first voltage applied to its gate to selectively supply driving current to the OLEDs; a second transistor for selectively transmitting the data signal to a first electrode of the first transistor according to a first scan signal; a third transistor for selectively permitting a flow of electric current to the first transistor so that the first transistor serves as a diode according to the first scan signal; a capacitor for storing the voltage applied to the gate of the first transistor while a data voltage is applied to the first electrode of the first transistor and for maintaining the stored voltage in the gate of the first transistor in a period when the OLEDs emit light; a fourth transistor for selectively transmitting an initializing signal to the capacitor according to a second scan signal; a fifth transistor for selectively transmitting the first power source to the first transistor according to a first emission control signal; a sixth transistor for selectively transmitting the first power source to the first transistor according to a second emission control signal; and a seventh transistor for selectively transmitting the first power source to the first transistor according to a third emission control signal.

[0017] According to a second aspect of the present invention, there is provided an organic light emitting display having a first pixel, a second pixel, and a third pixel. Each pixel includes a first, second and third OLED for emitting red, green, and blue light, respectively; a driving circuit commonly connected to the OLEDs for driving; and a sequential control circuit connected to the OLEDs and the driving circuit to sequentially control the driving of the first, second, and third OLEDs. The first, second, and third pixels are arranged to receive a data signal through a common data line, and an emission order of red, green, and blue light components of each pixel are different from one another. Moreover, the driving circuit includes: a first transistor including a first electrode, a second electrode, and third electrode connected to a first node, a second node, and a third node, respectively; a second transistor including a first electrode, a second electrode, and third electrode connected to a data line, the second node, and a first scan line, respectively; a third transistor including a first electrode, a second electrode, and a third electrode are connected to the first node, the third node, and the first scan line, respectively; a fourth transistor including a first electrode, a second electrode, and a third electrode connected to the third node, an initializing signal line, and a second scan line, respectively; a capacitor including a first electrode and a second electrodes connected to a first power source and the third node, respectively; a fifth transistor including a first electrode, a second electrode, and a third electrode connected to the first node, the first power source, and a first emission control line, respectively; a sixth transistor including a first electrode, a second electrode, and a third electrode connected to the second node, the first power source and a second emission control line, respectively; and a seventh transistor including a first electrode, a second electrode, and a third electrode connected to the second node, the first power source, and a third emission control line, respectively.

[0018] According to a third aspect of the present invention, there is provided an organic light emitting display having a first pixel, a second pixel, and a third pixel. Each pixel includes a first, second and third OLED for emitting red, green, and blue light, respectively; a driving circuit commonly connected to the OLEDs for driving; and a sequential control circuit connected to the OLEDs and the driving circuit to sequentially control the driving of the first, second, and third OLEDs. The first, second, and third pixels are arranged to receive a data signal through a common data line, and an emission order of red, green, and blue light components of each pixel are different from one another. In addition, the driving circuit comprises: a first transistor including a first electrode, a second electrode, and third electrode connected to a first node, a second node, and a third node, respectively; a second transistor including a first electrode, second electrode, and third electrode connected to a data line, the first node, and a first scan line, respectively; a third transistor including a first electrode, second electrode, and third electrode connected to the second node, the third node, and the first scan line, respectively; a fourth transistor including a first electrode, second electrode, and third electrode connected to the third node, an initializing signal line, and a second scan line respectively; a capacitor

including a first electrode and a second electrode connected to the first power source and the third node, respectively; a fifth transistor including a first electrode, second electrode, and third electrode connected to the second node, the first power source, and a first emission control line, respectively; a sixth transistor including a first electrode, second electrode, and third electrode connected to the second node, the first power source, and a second emission control line, respectively; and a seventh transistor including a first electrode, second electrode, and third electrode connected to the second node, the first power source, and a third emission control line, respectively.

BRIEF DESCRIPTION OF THE DRAWINGS

[0019] The objects and advantages of the invention will become apparent and more readily appreciated from the following description of the preferred embodiments, taken in view of the accompanying drawings.

[0020] FIG. 1 is a circuit diagram illustrating a section of a conventional organic light emitting display.

[0021] FIG. 2 illustrates the structure of an organic light emitting display according to an embodiment of the present invention.

[0022] FIG. 3 is a circuit diagram illustrating a first embodiment of the image display unit used for the organic light emitting display of FIG. 2.

[0023] FIG. 4 illustrates waveforms of signals transmitted to the image display unit of FIG. 3.

[0024] FIG. 5A, FIG. 5B, and FIG. 5C illustrate how the organic light emitting display of FIG. 3 emits light in accordance with the signals of FIG. 4 in one frame.

[0025] FIG. 6A, FIG. 6B, and FIG. 6C illustrate how the organic light emitting display of FIG. 3 emits light in one frame.

[0026] FIG. 7 is a circuit diagram illustrating a section of another embodiment of the image display unit used for the organic light emitting display of FIG. 2.

[0027] FIG. 8 illustrates waveforms of signals transmitted to the organic light emitting display of FIG. 7.

[0028] FIGS. 9A, FIG. 9B, and FIG. 9C illustrate that an organic light emitting display emits light in accordance with the signals of FIG. 8 in one frame.

[0029] FIG. 10 is a circuit diagram illustrating a pixel for which the driving circuit of FIG. 8 according to an embodiment is used.

[0030] FIG. 11 is a circuit diagram illustrating a pixel for which the driving circuit of FIG. 8 according to another embodiment is used.

[0031] FIG. 12 illustrates waveforms that illustrate the operations of the pixels of FIG. 10 and FIG. 11.

[0032] FIG. 13 illustrates waveforms that illustrate the operations of the pixels of FIG. 10 and FIG. 11 when the pixels are formed of NMOS transistors.

DETAILED DESCRIPTION OF THE INVENTION

[0033] Hereinafter, preferred embodiments of the present invention will be described with reference to the accompanying drawings.

[0034] Looking at **FIG. 2**, an organic light emitting display may include an image display unit **100**, a data driver **200**, and a scan driver **300**.

[0035] The image display unit **100** can include a plurality of pixels **110** and **120** comprising a plurality of OLEDs, a plurality of scan lines **S0**, **S1**, **S2** . . . **Sn-1**, and **Sn** arranged along a row direction, a plurality of first emission control lines **E11**, **E12** . . . **E1n-1**, and **E1n**, second emission control lines **E21**, **E22** . . . **E2n-1**, and **E2n**, and third emission control lines **E31**, **E32** . . . **E3n-1**, and **E3n** also arranged along the row direction, a plurality of data lines **D1**, **D2** . . . **Dm-1**, and **Dm** arranged along a column direction, and a plurality of pixel power source lines **Vdd** (not shown), which receive power sources from the outside to supply the pixel power sources.

[0036] The pixels **110** and **120** receive a scan signal from the adjacent scan lines **S0** to **Sn** and generate driving currents corresponding to data signals provided by data lines **D1** to **Dm**. The driving currents are transmitted to the OLEDs by emission control signals transmitted through the first emission control lines **E11** to **E1n** to the third emission control lines **E31** to **E3n** so that images are displayed.

[0037] In particular, adjacent first and second pixels **110** and **120** connected to one scan line **S1** are connected to one pixel power source line **Vdd** to receive a pixel power source.

[0038] The data driver **200** is connected to the data lines **D1** to **Dm** to transmit the data signals to the image display unit **100**. One data line sequentially transmits red, green, and blue data.

[0039] The scan driver **300** is connected to the scan lines **S0** to **Sn** and the first, second, and third emission control lines to sequentially transmit the scan signals and the emission control signals to the image display unit **100**.

[0040] As shown in **FIG. 3**, the first and second pixels **110** and **120** are connected to one data line **Dm**. The first and second pixels **110** and **120** may each include driving circuits **111** and **121**, switching circuits **112** and **122**, and first to third OLEDs (OLED1 to OLED3). OLED1, OLED2, and OLED3 emit red, green, and blue light components, respectively.

[0041] The driving circuits **111** and **121** may include a first transistor **M1**, a second transistor **M2**, and a capacitor **Cst**. The switching circuits **112** and **122** may include a first switching device **MR**, a second switching device **MG**, a third switching device **MB**, and first to third OLEDs (OLED1 to OLED3). OLED1, OLED2, and OLED3 emit red, green, and blue light components, respectively.

[0042] In the first pixel **110**, the source of the first transistor **M1** is connected to the pixel power source line **Vdd**, the drain is connected to a second node **B**, and the gate is connected to a first node **A** so that the current that flows through the second node **B** is determined by the voltage of the first node **A**.

[0043] The source of the second transistor **M2** is connected to the data line **Dm**, the drain is connected to the first node **A**, and the gate of the second transistor **M2** is connected to the scan line **Sn**.

[0044] The first electrode of the capacitor is connected to the pixel power source line and the second electrode of the capacitor is connected to the first node **A** so that the capacitor stores the voltage corresponding to difference between the pixel power source and the voltage of the first node **A**.

[0045] The source of the first switching device **MR** is connected to the second node **B**, the drain is connected to the OLED **1**, and the gate is connected to the first emission control line **E11** so that the first switching device **MR** selectively transmits the current that flows through the second node **B** to OLED1.

[0046] The source of the second switching device **MG** is connected to the second node **B**, the drain is connected to OLED2, and the gate is connected to the second emission control line **E21** so that the second switching device **MG** selectively transmits the current that flows through the second node **B** to OLED2.

[0047] The source of the third switching device **MB** is connected to the second node **B**, the drain is connected to OLED3, and the gate is connected to the third emission control line **E31** so that the third switching device **MB** selectively transmits the current that flows through the second node **B** to OLED3.

[0048] The second pixel **120**, is arranged similar to the first pixel **110**, but the switching devices **MR**, **MG**, and **MB** are respectively connected to emission control lines **E12**, **E22**, and **E32**.

[0049] Referring to **FIG. 4**, in operation, an image display unit receives first and second scan signals **s1** and **s2**, data signals, first, second and third emission control signals **e11**, **e21**, and **e31**, which are followed by first, second, and third emission control signals **e12**, **e22**, and **e32**. The scan signals and the emission control signals repeat, first, second and third periods **T1**, **T2** and **T3**.

[0050] First, in the first period **T1**, a red data signal is transmitted through a data line. At this time, when the red data signal is transmitted to the first node **A** through the first transistor **M1** of the first pixel **110** by the first scan signal **s1**, the capacitor **Cst** stores the voltage corresponding to difference between the pixel power source and the data signal and the voltage corresponding to EQUATION 1 is transmitted between the gate electrode and the source electrode of the first transistor **M1**.

$$V_{sg} = Vdd - Vdata \quad [EQUATION 1]$$

[0051] **Vsg**, **Vdd**, and **Vdata** represent the voltage between the gate electrode and the source electrode of the first transistor **M1**, the voltage of the pixel power source, and the voltage of the data signal, respectively.

[0052] Therefore, the current corresponding to EQUATION 2 flows through the second node **B**.

$$I = \frac{\beta}{2} (V_{gs} - |V_{th}|)^2 = \frac{\beta}{2} (Vdd - Vdata - |V_{th}|)^2 \quad [EQUATION 2]$$

$$\frac{\beta}{2} ((Vdd - Vdata) - |V_{th}|)^2 = \frac{\beta}{2} (Vdd - Vdata - |V_{th}|)^2$$

[0053] **Vgs**, **Vdd**, **Vdata**, **Vth**, and **P** represent the voltage between the gate electrode and the source electrode of the

first transistor M1, the voltage of the pixel power source, the voltage of the data signal, the threshold voltage of the first transistor, and the gain factor of the first transistor M1, respectively.

[0054] The current corresponding to the EQUATION 2 is transmitted to OLED1 of first pixel 110 by the first emission control signal e11 to emit red light.

[0055] A second pixel circuit is selected by the second scan signal s2 so that the red data signal is transmitted to the second pixel circuit and the current corresponding to the EQUATION 2 flows to the second node B. Current is transmitted to OLED1 of the second pixel circuit by the first emission control signal e12 so that red light is emitted.

[0056] In the second period T2, the first pixel circuit is selected by the first scan signal s1 so that a green data signal is transmitted. OLED2 of the first pixel circuit is selected by the second emission control signal e21 to emit green light.

[0057] The second pixel circuit is selected by the second scan signal s2 so that the green data signal is transmitted to the second pixel circuit and the current corresponding to the EQUATION 2 flows to the second node B. Current is transmitted to OLED2 by the second emission control signal e21 so that green light is emitted.

[0058] In the third period T3, the first pixel circuit is selected by the first scan signal s1 so that a blue data signal is transmitted. OLED3 of the first pixel circuit is selected by the third emission control signal e31 to emit blue light.

[0059] The second pixel circuit is selected by the second scan signal s2 so that the blue data signal is transmitted to the second pixel circuit. The current corresponding to the EQUATION 2 flows to the second node B. Current is transmitted to OLED3 by the third emission control signal e32 so that blue light is emitted.

[0060] Therefore, three OLEDs are controlled by a single pixel circuit, thereby reducing the number of pixel circuits required for the image display unit 100. As a result, it is possible to improve the aperture ratio of the image display unit 100. However, since the red light is emitted in the first period T1, the green light is emitted in the second period T2, and the blue light is emitted in the third period T3, only one color is emitted per period so that color breakup is generated. Also, since the current value varies with deviation in the threshold voltage of the first transistor M1, image quality can deteriorate.

[0061] FIG. 5A, FIG. 5B, and 5C illustrate first to third sub-fields included in one frame, respectively. As illustrated in FIG. 5A, red, green, and blue light components are emitted in the first sub-field. As illustrated in FIG. 5B, red, green, and blue light components are emitted in the second sub-field. As illustrated in FIG. 5C, red, green, and blue light components are emitted in the third sub-field. One row of each sub-field emits light components of the same color. Because all of the colors are displayed in each sub-field, however, color breakup is not significant.

[0062] Also, the emission control signals can be controlled so that light is emitted as illustrated in FIG. 6A, FIG. 6B, and FIG. 6C.

[0063] Turning to FIG. 7, three pixels are connected to one data line and three pixels are connected to one scan line

so that a total of nine pixels are displayed. The pixels are referred to as first to ninth pixels 110a through 110i, respectively. Each pixel may include a driving circuit 111, a switching circuit 112, and first to third OLEDs (OLED1 to OLED3). In each pixel, the driving circuit 111 receives the pixel power source Vdd, the data signals, and the scan signal s1 to generate current so that the current flows to the first node A.

[0064] The switching circuit 112 included in each pixel includes switching devices MR, MG, and MB. The source of the first switching device MR is connected to the first node A and the drain is connected to OLED1. The source of the second switching device MG is connected to the first node A and the drain is connected to OLED2. The source of the third switching device MB is connected to the first node A and the drain is connected to OLED3.

[0065] The first switching device MR of the first pixel 100a, the second switching device MG of the second pixel 100b, and the third switching device MB of the third pixel 100c are sequentially connected to the first emission control line E11. The second switching device MG of the first pixel 100a, the third switching device MB of the second pixel 100b, and the first switching device MR of the third pixel 100c are sequentially connected to the second emission control line E21. The third switching device MB of the first pixel 100a, the first switching device MR of the second pixel 100b, and the second switching device MG of the third pixel 100c are sequentially connected to the third emission control line E31.

[0066] The second switching device MG of the fourth pixel 100d, the third switching device MB of the fifth pixel 100e, and the first switching device MR of the sixth pixel 100f are sequentially connected to the first emission control line E12. The third switching device MB of the fourth pixel 100d, the first switching device MR of the fifth pixel 100e, and the second switching device MG of the sixth pixel 100f are sequentially connected to the second emission control line E22. The first switching device MR of the fourth pixel 100d, the second switching device MG of the fifth pixel 100e, and the third switching device MB of the sixth pixel 100f are sequentially connected to the third emission control line E32 that comes second.

[0067] The third switching device MB of the seventh pixel 100g, the first switching device MR of the eighth pixel 100h, and the second switching device MG of the ninth pixel 100i are sequentially connected to the first emission control line E13. The first switching device MR of the seventh pixel 100g, the second switching device MG of the eighth pixel 100h, and the third switching device MB of the ninth pixel 100i are sequentially connected to the second emission control line E22. The second switching device MB of the seventh pixel 100g, the third switching device MB of the eighth pixel 100h, and the first switching device MR of the ninth pixel 100i are sequentially connected to the third emission control line E33.

[0068] As shown in FIG. 8, the image display unit 100 first receives a first group of the first, second, and third emission control signals e11, e21, and e31, a second group of the first, second, and third emission control signals e12, e22, and e32 come next, and then a third group of the first, second, and third emission control signals e13, e23, and e33

to transmit currents to the OLEDs. The emission control signals repeat over the first, second, and third periods T1, T2, and T3.

[0069] In the first period T1, when the first scan signal s1 is transmitted to the driving circuit 111, the red, green, and blue data signals are transmitted through the first, second, and third data lines D1, D2, and D3, respectively, so that the first, second, and third pixels 100a, 100b, and 100c emit the red, green, and blue light components, respectively.

[0070] When the second scan signal s2 is transmitted to the driving circuit 111, the green, blue, and red data signals are transmitted through the first, second, and third data lines D1, D2, and D3, respectively, so that the fourth, fifth, and sixth pixels 100d, 100e, and 100f emit the green, blue, and red light components, respectively.

[0071] When the third scan signal s3 is transmitted to the driving circuit 111, the blue, red, and green data signals are transmitted through the first, second, and third data lines D1, D2, and D3, respectively, so that the seventh, eighth, and ninth pixels 100g, 100h, and 100i emit the blue, red, and green light components, respectively.

[0072] In the second period T2, when the first scan signal s1 is transmitted to the driving circuit 111, the green, blue, and red data signals are transmitted through the first, second, and third data lines D1, D2, and D3, respectively, so that the first, second, and third pixels 100a, 100b, and 100c emit the green, blue, and red light components, respectively.

[0073] When the second scan signal s2 is transmitted to the driving circuit 111, the blue, red, and green data signals are transmitted through the first, second, and third data lines D1, D2, and D3, respectively, so that the fourth, fifth, and sixth pixels 100d, 100e, and 100f emit the blue, red, and green light components, respectively.

[0074] When the third scan signal s3 is transmitted to the driving circuit 111, the red, green, and blue data signals are transmitted through the first, second, and third data lines D1, D2, and D3, respectively, so that the seventh, eighth, and ninth pixels 100g, 100h, and 100i emit the red, green, and blue light components, respectively.

[0075] In the third period T3, when the first scan signal s1 is transmitted to the driving circuit 111, the blue, red, and green data signals are transmitted through the first, second, and third data lines D1, D2, and D3, respectively, so that the first, second, and third pixels 100a, 100b, and 100c emit the blue, red, and green light components, respectively.

[0076] When the second scan signal s2 is transmitted to the driving circuit 111, the red, green, and blue data signals are transmitted through the first, second, and third data lines D1, D2, and D3, respectively, so that the fourth, fifth, and sixth pixels 100d, 100e, and 100f emit the red, green, and blue light components, respectively.

[0077] When the third scan signal s3 is transmitted to the driving circuit 111, the green, blue, and red data signals are transmitted through the first, second, and third data lines D1, D2, and D3, respectively, so that the seventh, eighth, and ninth pixels 100g, 100h, and 100i emit the green, blue, and red light components, respectively.

[0078] FIG. 9A, FIG. 9B, and FIG. 9C illustrate first to third sub-fields included in one frame, respectively. As

illustrated in FIG. 9A, red, green, and blue light components are emitted in the first sub-field. As illustrated in FIG. 9B, red, green, and blue light components are emitted in the second sub-field. As illustrated in FIG. 9C, red, green, and blue light components are emitted in the third sub-field.

[0079] One row of each sub-field emits the red, green, and blue light components, which is different from the sub-fields shown in FIGS. 5A to 5C or FIGS. 6A to 6C, so that color breakup is not generated.

[0080] Referring to FIG. 10, the pixel circuit, may include first to seventh transistors M1 to M7, first to third switching devices MR, MG, and MB, and a capacitor Cst. Each transistor and switching device may include a source, a drain, and a gate. The capacitor Cst may include a first electrode and a second electrode. Since the drains and sources of the first to seventh transistors M1 to M7 and the first to third switching devices MR, MG, and MB have no physical differences, each source and drain may be referred to as a first electrode and a second electrode.

[0081] The drain of the first transistor M1 is connected to a first node A, the source is connected to a second node B, and the gate is connected to a third node C so that current flows from the second node B to the first node A in accordance with the voltage of the third node C.

[0082] The source of the second transistor M2 is connected to the data line Dm, the drain is connected to the second node B, and the gate is connected to the first scan line Sn so that the second transistor M2 performs a switching operation in accordance with the scan signal transmitted through the first scan line Sn to selectively transmit the data signal transmitted through the data line Dm to the second node B.

[0083] The source of the third transistor transistor transistor M3 is connected to the first node A, the drain is connected to the third node C, and the gate is connected to the first scan line Sn so that the potential of the first node A is made equal to the potential of the third node C by the scan signal transmitted through the first scan line Sn. Therefore, electric current flows through the first transistor M1 so that the first transistor M1 serves as a diode.

[0084] The source and gate of the fourth transistor M4 are connected to the second scan line Sn-1 and the drain is connected to the third node C so that the fourth transistor M4 transmits an initializing signal to the third node C. The initial signal is the scan signal sn-1 input to the row that precedes the row from which the first scan line Sn inputs a scan signal by one row.

[0085] The source of the fifth transistor M5 is connected to the pixel power source line Vdd, the drain is connected to the second node B, and the gate is connected to the first emission control line E1n so that the fifth transistor M5 selectively transmits the pixel power source to the second node B according to the emission control signal transmitted through the first emission control line E1n.

[0086] The source of the sixth transistor M6 is connected to the pixel power source line Vdd, the drain is connected to the second node B, and the gate is connected to the second emission control line E2n so that the sixth transistor M6 selectively transmits the pixel power source to the second

node B according to the emission control signal transmitted through the second emission control line E_{2n}.

[0087] The source of the seventh transistor M₇ is connected to the pixel power source line Vdd, the drain is connected to the second node B, and the gate is connected to the third emission control line E_{3n} so that the seventh transistor M₇ selectively transmits the pixel power source to the second node B according to the emission control signal transmitted through the third emission control signal E_{3n}.

[0088] The source of the first switching device MR is connected to the first node A, the drain is connected to OLED 1, and the gate is connected to the first emission control line E_{1n} so that the first switching device MR transmits the current that flows through the first node A to OLED1 according to the emission control signal transmitted through the first emission control line E_{1n} to emit light from OLED1.

[0089] The source of the second switching device MG is connected to the first node A, the drain is connected to OLED2, and the gate is connected to the second emission control line E_{2n} so that the second switching device MG transmits the current that flows through the first node A to OLED2 according to the emission control signal transmitted through the second emission control line E_{2n} to emit light from OLED2.

[0090] The source of the third switching device MB is connected to the first node A, the drain is connected to OLED3, and the gate is connected to the third emission control line E_{3n} so that the third switching device MB transmits the current that flows through the first node A to OLED3 according to the emission control signal transmitted through the third emission control line E_{3n} to emit light from OLED3.

[0091] The first electrode of the capacitor Cst is connected to the pixel power source line Vdd and the second electrode is connected to the third node C so that the capacitor Cst is initialized by the initializing signal transmitted to the third node C through the fourth transistor M₄ and the voltage corresponding to the data signal is stored and transmitted to the third node C. Therefore, the gate voltage of the first transistor M₁ is maintained for a predetermined time by the capacitor Cst.

[0092] Referring to FIG. 11, another pixel circuit is shown that also may include first to seventh transistors M₁ to M₇ and a capacitor Cst. Only the differences between the pixel illustrated in FIG. 10 and the pixel illustrated in FIG. 11 will now be described.

[0093] Here, the source of the second transistor M₂ is connected to the data line Dm, the drain is connected to the first node A, and the gate is connected to the first scan line Sn so that the second transistor M₂ performs a switching operation in accordance with the scan signal transmitted through the first scan line Sn to selectively transmit the data signal transmitted through the data line Dm to the first node A.

[0094] The source of the third transistor M₃ is connected to the second node B, the drain is connected to the third node C, and the gate is connected to the first scan line Sn so that the potential of the second node B is made equal to the potential of the third node C by the scan signal transmitted

through the first scan line Sn. Therefore, electric current flows through the first transistor M₁ so that it serves as a diode.

[0095] The source of the fourth transistor M₄ is connected to the anode electrode of OLEDs, the drain is connected to the third node C, and the gate is connected to the second scan line Sn-1 so that the fourth transistor M₄ transmits a voltage, when no current flows to the first to third OLEDs (OLED1 to OLED3), to the third node C in accordance with the scan signal from the second scan line Sn-1. At this time, the voltage transmitted to the third node C in accordance with the scan signal from scan line Sn-1 is used as an initializing signal for initializing the capacitor Cst.

[0096] Referring to FIG. 12, the pixel is operated using first and second scan signals sn and sn-1, the data signals, and the first, second, and third emission control signals e_{1n}, e_{2n}, and e_{3n}. The first and second scan signals sn and sn-1 and the first to third emission control signals e_{1n} to e_{3n} are periodical signals and the second scan signal sn-1 is transmitted to a scan line that precedes the scan line to which the first scan signal sn is transmitted.

[0097] In the first time period T₁, first, when the fourth transistor M₄ is turned on by the second scan signal sn-1, in the case of FIG. 8, the second scan signal sn-1 is transmitted to the capacitor Cst through the fourth transistor M₄ so that the capacitor Cst is initialized. In the pixel of FIG. 11, the voltage applied to the OLEDs when they do not emit light is transmitted to the capacitor Cst through the fourth transistor M₄ so that the capacitor Cst is initialized.

[0098] Second, the second and third transistors M₂ and M₃ are turned on by the first scan signal sn so that the potential of the second node B is made equal to the potential of the third node C. Therefore, electric current flows through the first transistor M₁ so that it serves as a diode. As a result, the data signal is transmitted to the second electrode of the capacitor Cst through the second transistor M₂, the first transistor M₁, and the third transistor M₃ so that the voltage corresponding to difference between the data signal and the threshold voltage is transmitted to the second electrode of the capacitor Cst.

[0099] After the first scan signal sn is transmitted at a high level, when the first emission control signal e_{1n} is transmitted at a low level for a predetermined period, the fifth and sixth transistors M₅ and M₆ are turned on by the first emission control signal e_{1n} so that the voltage corresponding to EQUATION 3 is applied between the gate and source of the first transistor M₁.

$$V_{sg} = Vdd - (V_{data} - V_{th}) \quad [EQUATION 3]$$

[0100] V_{sg}, Vdd, V_{data}, and V_{th} represent the voltage between the source and gate electrodes of the first transistor M₁, the voltage of the pixel power source, the voltage of the data signal, and the threshold voltage of the first transistor M₁, respectively.

[0101] The sixth transistor M₆ is turned on so that current corresponding to EQUATION 4 flows to the OLEDs.

$$I = \frac{\beta}{2} (V_{gs} - V_{th})^2 = \quad [EQUATION 4]$$

-continued

$$\frac{\beta}{2}(V_{data} - V_{dd} + V_{th} - V_{th})^2 = \frac{\beta}{2}(V_{data} - V_{dd})^2$$

[0102] I, V_{gs}, V_{dd}, V_{th}, and V_{data} represent the current that flows through the first node A, the voltage applied to the gate of the first transistor M1, the voltage of the pixel power source, the threshold voltage of the first transistor M1, and the voltage of the data signal, respectively.

[0103] Therefore, the current that flows to the first node A flows regardless of the threshold voltage of the first transistor M1.

[0104] Similar to the current in the first time period T1, the currents in the second time period T2 and the third time period T3 flow from the first node A to the second and third OLEDs (OLED2 and OLED3, respectively) by the second and third emission control signals e2n and e3n, respectively.

[0105] Here, the pixels illustrated in FIGS. 11 and 12 are formed of PMOS transistors. When the pixels are to be formed of NMOS transistors, the waveforms illustrated in FIG. 13 are input.

[0106] Although preferred embodiments of the present invention have been shown and described, it would be appreciated by those skilled in the art that changes might be made to the embodiments described herein without departing from the principles and spirit of the invention, the scope of which is defined in the appended claims and their equivalents.

What is claimed is:

1. An organic light emitting display comprising a first pixel, a second pixel, and a third pixel, each pixel comprising:

a first organic light emitting diode (OLED), a second OLED, and a third OLED for emitting red, green, and blue light, respectively;

a driving circuit commonly connected to the first, second, and third OLEDs for driving thereof; and

a switching circuit connected to the OLEDs and the driving circuit to sequentially control the driving of the first, second, and third OLEDs,

wherein the first pixel, the second pixel, and the third pixel are arranged to receive a data signal through a common data line, and an emission order of red, green, and blue light components of the first, second, and third pixels are different from each other, and

wherein the driving circuit comprises:

a first transistor for receiving a first power source corresponding to a first voltage applied to a gate thereof to selectively supply driving current to the OLEDs;

a second transistor for selectively transmitting the data signal to a first electrode of the first transistor according to a first scan signal;

a third transistor for selectively permitting a flow of electric current to the first transistor so that the first transistor serves as a diode according to the first scan signal;

a capacitor for storing the voltage applied to the gate of the first transistor while a data voltage is applied to the first electrode of the first transistor and for maintaining the stored voltage in the gate of the first transistor in a period when the OLEDs emit light;

a fourth transistor for selectively transmitting an initializing signal to the capacitor according to a second scan signal;

a fifth transistor for selectively transmitting the first power source to the first transistor according to a first emission control signal;

a sixth transistor for selectively transmitting the first power source to the first transistor according to a second emission control signal; and

a seventh transistor for selectively transmitting the first power source to the first transistor according to a third emission control signal.

2. The organic light emitting display of claim 1, wherein the switching circuit comprises:

a first switching device including a first electrode connected to the driving circuit and a second electrode connected to the first OLED;

a second switching device including a first electrode connected to the driving circuit and a second electrode connected to the second OLED; and

a third switching device including a first electrode connected to the driving circuit and a second electrode connected to the third OLED,

wherein gates of each of the first, second, and third switching devices receive different emission control signals among the first, second, and third emission control signals, respectively, to operate.

3. The organic light emitting display of claim 1, wherein the second scan signal is transmitted to a second scan line that precedes a first scan line to which the first scan signal is transmitted.

4. The organic light emitting display of claim 1, wherein an initializing voltage is transmitted by the second scan signal.

5. The organic light emitting display of claim 4, wherein the initializing voltage is a voltage applied to the OLEDs when no current flows through the first transistor.

6. The organic light emitting display of claim 1, further comprising a scan driver transmitting the scan signals and the emission control signals.

7. The organic light emitting display of claim 1, further comprising a data driver transmitting the data signal.

8. The organic light emitting display of claim 7,

wherein the data driver transmits data signals having information on red, green, and blue light components during a first period, a second period, and a third period through a data line, wherein the data signals are transmitted in the order of red, green, and blue in the first period,

wherein the data signals are transmitted in the order of green, blue, and red in the second period, and

wherein the data signals are transmitted in the order of blue, red, and green in the third period.

9. The organic light emitting display as claimed in claim 7, wherein the data driver transmits red, green, and blue data to the data lines when one of the scan signals is transmitted to the driving circuits of the respective first, second, and third pixels.

10. An organic light emitting display comprising a first pixel, a second pixel, and a third pixel, each pixel comprising:

a first organic light emitting diode (OLED), a second OLED, and a third OLED;

a driving circuit commonly connected to the first, second, and third OLEDs for driving thereof; and

a sequential control circuit connected to the OLEDs and the driving circuit to sequentially control the driving of the first, second, and third OLEDs,

wherein the first pixel, the second pixel, and third pixel are arranged to receive a data signal through a common data line, and an emission order of red, green, and blue light components of the first, second, and third pixels are different from each other, and

wherein the driving circuit comprises:

a first transistor including a first electrode, a second electrode, and a third electrode connected to a first node, a second node, and a third node, respectively;

a second transistor including a first electrode, a second electrode, and a third electrode connected to a data line, the second node, and a first scan line, respectively;

a third transistor including a first electrode, a second electrode, and a third electrode are connected to the first node, the third node, and the first scan line, respectively;

a fourth transistor including a first electrode, a second electrode, and a third electrode connected to the third node, an initializing signal line, and a second scan line, respectively;

a capacitor including a first electrode and a second electrodes connected to a first power source and the third node, respectively;

a fifth transistor including a first electrode, a second electrode, and a third electrode connected to the first node, the first power source, and a first emission control line, respectively;

a sixth transistor including a first electrode, a second electrode, and a third electrode connected to the second node, the first power source and a second emission control line, respectively; and

a seventh transistor including a first electrode, a second electrode, and a third electrode connected to the second node, the first power source, and a third emission control line, respectively.

11. The organic light emitting display of claim 10, wherein the the sequential control circuit comprises:

a first switching device including a first electrode connected to the driving circuit and a second electrode connected to the first OLED;

a second switching device including a first electrode connected to the driving circuit and a second electrode connected to the second OLED; and

a third switching device including a first electrode connected to the driving circuit and a second electrode connected to the third OLED,

wherein gates of each of the first, second, and third switching devices are connected to different emission control lines among the first, second, and third emission control lines.

12. The organic light emitting display of claim 10, wherein a second scan signal is transmitted to the second scan line that precedes the first scan line to which a first scan signal is transmitted.

13. The organic light emitting display of **10**, wherein the initializing signal line is connected to an anode electrode of at least one OLED.

14. The organic light emitting display of claim 10, further comprising a scan driver transmitting scan signals through the scan lines and emission control signals through the emission control lines.

15. The organic light emitting display of claim 10, further comprising a data driver transmitting data signals through the data line.

16. The organic light emitting display of claim 15,

wherein the data driver transmits data signals having information on red, green, and blue light components during a first period, a second period, and a third period through the data line,

wherein the data signals are transmitted in the order of red, green, and blue in the first period,

wherein the data signals are transmitted in the order of green, blue, and red in the second period, and

wherein the data signals are transmitted in the order of blue, red, and green in the third period.

17. The organic light emitting display as claimed in claim 15, wherein the data driver transmits red, green, and blue data to the data lines when a scan signal from one of the scan lines is transmitted to the driving circuits of the respective first, second, and third pixels.

18. An organic light emitting display comprising a first pixel, a second pixel, and a third pixel, each pixel comprising:

a first organic light emitting diode (OLED), a second OLED, and a third OLED;

a driving circuit commonly connected to the first, second, and third OLEDs for driving thereof; and

a sequential control circuit connected to the OLEDs and the driving circuit to sequentially control the driving of the first, second, and third OLEDs,

wherein the first pixel, the second pixel, and the third pixel are arranged to receive a data signal through a common data line, and the emission order of red, green, and blue light components of the first, second, and third pixels are different from each other, and

wherein the driving circuit comprises:

- a first transistor including a first electrode, a second electrode, and a third electrode connected to a first node, a second node, and a third node, respectively;
- a second transistor including a first electrode, a second electrode, and a third electrode connected to a data line, the first node, and a first scan line, respectively;
- a third transistor including a first electrode, a second electrode, and a third electrode connected to the second node, the third node, and the first scan line, respectively;
- a fourth transistor including a first electrode, a second electrode, and a third electrode connected to the third node, an initializing signal line, and a second scan line respectively;
- a capacitor including a first electrode and a second electrode connected to the first power source and the third node, respectively;
- a fifth transistor including a first electrode, a second electrode, and a third electrode connected to the second node, the first power source, and a first emission control line, respectively;
- a sixth transistor including a first electrode, a second electrode, and a third electrode connected to the second node, the first power source, and a second emission control line, respectively; and
- a seventh transistor including a first electrode, a second electrode, and a third electrode connected to the second node, the first power source, and a third emission control line, respectively.

19. The organic light emitting display of claim 18, wherein the sequential control circuit comprises:

- a first switching device including a first electrode connected to the driving circuit and a second electrode connected to the first OLED;
- a second switching device including a first electrode connected to the driving circuit and a second electrode connected to the second OLED; and

a third switching device including a first electrode connected to the driving circuit and a second electrode connected to the third OLED,

wherein gates of each of the first, second, and third switching devices are connected to different emission control lines among the first, second, and third emission control lines.

20. The organic light emitting display of 18, wherein a second scan signal is transmitted to the second scan line that precedes the first scan line to which a first scan signal is transmitted.

21. The organic light emitting display of claim 18, wherein the initializing signal line is connected to an anode electrode of at least one of the OLEDs.

22. The organic light emitting display of claim 18, further comprising a scan driver transmitting scan signals through the scan lines and emission control signals through the emission control lines.

23. The organic light emitting display as claimed in any one of claims 18, further comprising a data driver transmitting the data signal through the data line.

24. The organic light emitting display of claim 23,

wherein the data driver transmits data signals having information on red, green, and blue light components during a first period, a second period, and a third period through the data line,

wherein the data signals are transmitted in the order of red, green, and blue in the first period,

wherein the data signals are transmitted in the order of green, blue, and red in the second period, and

wherein the data signals are transmitted in the order of blue, red, and green in the third period.

25. The organic light emitting display of claim 24, wherein the data driver transmits red, green, and blue data to the data lines when a scan signal from one of the scan lines is transmitted to the driving circuits of the respective first, second, and third pixels.

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专利名称(译)	像素电路和有机发光显示器		
公开(公告)号	US20060139257A1	公开(公告)日	2006-06-29
申请号	US11/296339	申请日	2005-12-08
[标]申请(专利权)人(译)	KWAK WONKYOUNG 朴成权		
申请(专利权)人(译)	KWAK WONKYOUNG 朴成权		
当前申请(专利权)人(译)	三星DISPLAY CO. , LTD.		
[标]发明人	KWAK WON KYU PARK SUNG CHEON		
发明人	KWAK, WON KYU PARK, SUNG CHEON		
IPC分类号	G09G3/30		
CPC分类号	G09G3/2022 G09G3/3233 G09G2300/0465 G09G2300/0804 G09G2300/0819 G09G2300/0842 G09G2300/0861 G09G2310/0218 G09G2310/0235 G09G2320/0242		
优先权	1020040103817 2004-12-09 KR		
其他公开文献	US7535447		
外部链接	Espacenet USPTO		

摘要(译)

有机发光显示器包括像素电路，该像素电路具有分别用于发射红光，绿光和蓝光的第一，第二和第三有机发光二极管(OLED)，共同连接到OLED的驱动电路，以及连接的开关电路OLED和驱动电路依次控制其驱动。通过控制多个OLED，减少了有机发光显示器中的像素电路的数量，从而减少了扫描线，数据线和发光控制线的数量，这又改善了发光显示器的开口率。此外，控制OLED的发光顺序，从而可以防止产生色乱。

